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9			RM		First Named Invent	tor K	evin T. Look	
	(to be used t	or all corresp	oondence after initial fi	ing)	Examiner Name	U	nknown	
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	Express Mail Receipt No.				Patent No.			
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This collection of information is /required by 37 CFR 1.17 and 1.27. The information is required to botain or retain a benefit by the public which is to file (and by the USPTC) to process) an application. Confidentially is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is entired to lase 12 cm of the USPTC or the USP

IN THE UNITED STATES PATENT OFFICE

Applicants: Kevin T. Look et al.

Assignee: Xilinx, Inc.

Title: "Regulating Unused/Inactive Resources in

Programmable Logic Devices for Static Power

Reduction"

Serial No.: 10/783,589 File Date: 02/20/2004

Examiner: Unknown Art Unit: 2171
Docket No.: X-1462-2P US Conf. No.: 1594

COMMISSIONER FOR PATENTS

P.O. Box 1450

Alexandria, Virginia 22313-1450

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Pursuant to 37 C.F.R. 1.56, Applicants bring to the attention of the Examiner the six (6) references listed in the attached Substitute for Form PTO-1449A/PT. One copy each of the four non-patent references is enclosed herein.

This Information Disclosure Statement is being filed under 37 CFR 1.97(b) prior to the receipt of a first office action.

Citation of the above documents shall not be construed as an admission that the documents are necessarily prior art with respect to the instant invention. Citation of the above documents shall not be construed as a representation that a search has been made other than as described above. Also, the citation of the above documents shall not be construed as an admission that the information cited herein is, or is considered to be, material to patentability as defined in \$1.56(b).

Respectfully submitted,

Justin Liu

Attorney for Applicants Reg. No. 51,959

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450,

Alexandria, Virginia 22313-1450, on July 30, 2004.

Julie Matthews Name

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ork Reduction Act of 1995, no persons ere required to respond to a collection of information unless to creatings a visit OMB control number.

Substitute for form 1449A/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

Sheet 1 of 2

	Con	nplete if Known		
	Application / Conf. No.	10/783,589 / 1594		
:	Filing Date	February 20, 2004		
-	First Named Inventor	Kevin T. Look		
	Art Unit	2171		
	Examiner Name	Unknown		
_	Attorney Docket Number	X-1462-2P US		

			U.S. PATENT	DOCUMENTS	
Examiner	Cite No.'	Document Number Number - Kind Code (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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Examiner Signature		/Daniel Chang/		Date Considered	09/02/2008	

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*Against, unique citation reagration number (optional) *See Kinds of USPTO Patent Documents at www.uppin.opr or MPEP 001.04.* Enter Office hall issued the document, by the five-liker code (MPEP Standards 513... *For Jacobsen patent document, by the indication of the year of the reing of the more must proceed the serial number of the patent document. *Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16.if possible. *Application is to place a check mark her of it English language Translation is attached.

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Sheet 2 of 2

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	Filing Date	February 20, 2004
	First Named Inventor	Kevin T. Look
	Art Unit	2171
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	Attorney Docket Number	X-1462-2P US

OTHER NON PATENT LITERATURE DOCUMENTS				
Examiner Initials *	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the term (book, magazine, journal, serial, symposium, catalog, ac.), date, page(s), volume-issue number(s), publisher, city and/or country whene publisher.	T²	
/DC/		Jae Y. Park, Mark G. Allen; "A Comparison of Micromachined Inductors with Different Magnetic Core Materials"; 1996 Electronic Components and Technology Conference; 1996 IEEE; pp. 375-381.		
/DC		Thomas D. Burd et al.; "A Dynamic Voltage Scaled Microprocessor System"; 2000 IEEE; IEEE Journal of Solid-State Circuits, Vol. 35, No. 11, November 2000; pp. 1571-1580.		
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/DC/		Anthony John Stratakos; "High-Efficiency Low-Voltage DC-DC Conversion for Portable Applications," Chapter 3; "DC-DC Converter Fundamentals"; pp. 42-78. no date		
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Examir Signati		/Daniel Chang/ Date Considered 09/02/2008	J	

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